Workshop on Advances on Analog Circuit Design

AUCD 2008
Aula Volta – Pavia, Italy
April, 8-10, 2008
www.microelectronicsevents.com/aacd08

Program

April, 8th, 2008
“High-speed clock and data recovery”
Chairman: Prof. Michiel Steyaert (KU Leuven)

8.45 Opening
9.15 Anthony Sanders (Infineon), "Fundamental Stochastic Jitter Processes associated with Clock and Data Recovery: A Tutorial"
10.00 Massimo Pozzoni (STMicroelectronics), "Clock Recovery and Equalization Techniques for Lossy Channels in Multi Gb/s Serial Links"
10.45 Coffee break
11.15 J.Crois (AnSem), "Top-down bottom-up design methodology for fast and reliable Serdes developments in nm technologies"
12.00 Michael Perrott (MIT), "Mixed-Signal Implementation Strategies for High Performance Clock and Data Recovery Circuits"
12.45 Lunch
14.00 Song Wu (Xilinx), "Jointly Optimize Equalizer and CDR for Multi-gigabits SerDes"
14.45 J.Daniels (KU Leuven), "Time-to-digital conversion: an alternative view on synchronisation"
15.30 Coffee break
16.00 Panel discussion
17.00 Visit to Volta Museum
19.30 Social dinner
April, 9th, 2008
“High-performance amplifiers”
Chairman: 1

9.00 Johan Huijsing (Delft University of Technology), “Instrumentation Amplifier Developments”
9.45 Wilco Kindt (National Semiconductor), “Current sense amplifiers with extended common mode voltage range”
10.30 Coffee break

11.00 Ramón González Carvajal (Universidad de Sevilla), “Low Voltage, Power Efficient Amplifiers for Emerging Technologies”
11.45 Timothy Denison (Medtronic), “Circuit Architectures for Efficient Coupling to the Nervous System”

12.30 Lunch

14.00 Marco Sampietro (Politecnico di Milano), “Transimpedance amplifiers for extremely high sensitivity impedance measurements on nanodevices”
15.30 Coffee break

April, 10th, 2008
“Power management”
Chairman: Herman Casier

8.30 Franco Maloberti (Univ. of Pavia), “Single-Inductor Multiple-Output DC-DC Converters”
10.00 Coffee break
11.30 Ivan Koudar (AMI Semiconductors), “Robust Automotive Buck-Boost converter”
11.15 Mario Manninger (AustriaMicroSystems) “A Highly Integrated PMIC in Advanced CMOS”

12.00 Lunch
13.00 Eduard Alarcon (Univ. of Barcelona) “Wideband efficient amplifiers for on-chip adaptive power management applications”
13.45 Vadim Ivanov (Texas Instruments), “Design methodology and circuit techniques of the any-load stable LDOs with instant load regulation and low noise”
14.30 Panel discussion

Scientific Committee
Herman Casier
AMI Semiconductor, (Belgium)
Michiel Steyaert
Catholic Univ., Leuven (Belgium)
Arthur van Roermund,
Univ. of Technology Eindhoven
(The Netherlands)

Organizing Committee
Andrea Baschirotto
Univ. of Salento
Rinaldo Castello
Univ. of Pavia
Piero Malcovati
Univ. of Pavia

Contact informations
Andrea Baschirotto
E-mail: andrea.baschirotto@unipv.it
Phone: (+39)-338-4153105
FAX: (+39)-0382-422.583
AACD Series web-site: www.aacd.ws
AACD2008web-site: www.microelectronicsevents.com/aacd08